

**Amendments to the Specification:**

Please replace the paragraph starting on page 4, line 24, with the following amended paragraph:

FIG. 4 shows an integrated circuit comprising the electronic circuit arrangement. The integrated circuit 400 comprises an electronic circuit arrangement 450 according to the invention. The electronic circuit arrangement 450 comprises an asynchronous processor 451 and a clock fail detection circuit (CLK FAIL)-~~451~~452. The integrated circuit further comprises a clock generation circuit 404 and additional electronic circuit arrangements HW1 401, HW2 402, HW3 403.

Please replace the paragraph starting on page 5, line 6, with the following amended paragraph:

Electronic circuit arrangement HW2 is asynchronous and therefore does not require a clock signal for its operation. For communication with processor 451 it comprises-~~on~~one or more inputs and/or outputs HW2 IO.

Please replace the paragraph starting on page 5, line 9, with the following amended paragraph:

Electronic circuit arrangement HW3 is also asynchronous and therefore does not require a clock signal for its operation. For communication with processor 451 it comprises-~~on~~one or more inputs and/outputs HW3 IO.